

Term End Examination Time Table Even Semester Backlog 2026

SCHOOL OF COMPUTER SCIENCE AND ENGINEERING

School : School of Computer Science and Engineering

Class : First Year M.Tech

Semester : II (Backlog)

Sr. No.	Specialization	Date	Day	Time	Course Code	Course Name
1	FY M.Tech AIOM	27-Oct-2026	Tuesday	08:00 AM to 09:30 AM	AIO50050	Nature Inspired Optimization Methods-I
2	FY M.Tech Cyber Security	27-Oct-2026	Tuesday	08:00 AM to 09:30 AM	CSC40060	Vulnerability Assessment and Penetration Testing
3	FY M.Tech DSA	27-Oct-2026	Tuesday	08:00 AM to 09:30 AM	CSD40060/CSD7PM06A	Natural Language Processing
4	FY M.Tech AIOM	28-Oct-2026	Wednesday	08:00 AM to 09:30 AM	AIO50060	Artificial Neural Networks
5	FY M.Tech Cyber Security	28-Oct-2026	Wednesday	08:00 AM to 09:30 AM	CSC40070	Digital Forensics Analysis
6	FY M.Tech DSA	28-Oct-2026	Wednesday	08:00 AM to 09:30 AM	CSD40070/CSD7PM07A	Data Analytics for Data Science
7	FY M.Tech Cyber Security	29-Oct-2026	Thursday	08:00 AM to 09:30 AM	CSC40090	Advanced Security Management and Cyber Laws
8	FY M.Tech Cyber Security	29-Oct-2026	Thursday	08:00 AM to 09:30 AM	CSC40080	Data Privacy and Compliances
9	FY M.Tech DSA	29-Oct-2026	Thursday	08:00 AM to 09:30 AM	CSD40090/CSD7PE12A	Reinforcement Learning
10	FY M.Tech Cyber Security / DSA	31-Oct-2026	Saturday	09:30 AM to 10:30 AM	PCE10050	Peace Building: Global Initiatives *

Legends : * Online Examination

Note : 1) Any unfair means during the examination is punishable as per the University rules and regulations.

2) University authorities reserves rights for any change in schedule.

3) Students should report to the Examination venue 1 hour before the start of examination.

4) Please visit website : <https://mitwpu.edu.in/academics/exam-timetable> for regular updates and examination related information.



Dr. Veera Talukdar
Controller of Examination
MIT WPU, Pune - 411038