



End Term Examination Time Table 2024-25

Name of School / Faculty : School of Engineering and Technology

Examination Month-Year : Nov/Dec 2024

Class:

First Year M.Tech

Semester: I & II (Backlog)

Exam Time Descriptive Type (D) : 1 Hrs.30 mins Objective Type (O) : 1 hr	Date	25 November 2024	26 November 2024	27 November 2024
	Day	Monday	Tuesday	Wednesday
	Specialization/ Trimester	FY M.Tech (Backlog)		
10:30 am to 12:00 pm	FY M.Tech.VLSI	ECE7PM01A Analog and Digital VLSI Design (D)	ECE7PM08A Testing and Testability (D)	ECE7PE11A Advanced VLSI Design (D)
10:30 am to 12:00 pm	FY M.Tech.by Research (eMobility)	EEE7PM06A Advanced topics in EV (D)	-	-

Note : 1) Any unfair means during the examination is punishable as per the University rules and regulations.

2) University authorities reserves rights for any change in schedule.

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Controller of Examination
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